

**METHODS FOR ANNEALING A SUBSTRATE AND  
ARTICLE PRODUCED BY SUCH METHODS**

**Background of the Invention**

**5 1. Field of the Invention**

This invention is directed to methods for annealing a substrate using a pulse of particles such as electrons, protons, ions, or other, neutral particles. The invention is also directed to integrated devices or circuits made by such methods. The substrate can be composed of silicon, gallium arsenide, or other semiconductor species, or can  
10 include an insulative material with a relatively thin-film of semiconductor material formed thereon. The apparatus and methods can be used to anneal or activate integrated devices or circuits formed in the substrate, and to form low-resistance silicide regions thereon. Such integrated devices can include active devices such as transistors or diodes, or passive devices such as resistors or capacitors. For example,  
15 the apparatus and method can be used to anneal a substrate to obtain a relatively high crystalline state therein, or to activate the substrate to incorporate dopant atoms into the substrate's crystalline lattice, to achieve proper electrical performance of the integrated devices.

**20 2. Description of the Related Art**

In forming integrated devices in a semiconductor substrate it is common to use ion implantation to introduce dopant atoms of appropriate species into the substrate to form n-type or p-type regions. Immediately after implantation the dopant atoms are not generally positioned within the substrate where they can be useful to achieve  
25 proper electrical performance of the integrated device being formed. Also, the dopant atoms are accelerated to relatively high-energies by ion implanters. Therefore, upon colliding with the substrate the dopant atoms cause considerable damage to the

substrate by breaking chemical bonds therein. The crystalline structure of the substrate must be restored, and the dopant atoms incorporated into it, in order to obtain proper electrical performance of the integrated devices. Annealing is typically performed by heating the substrate so that the dopant atoms and substrate atoms  
5 have sufficient energy to realign. Of considerable importance in the manufacture of integrated devices or circuits is the "throughput" that a manufacturer can obtain with a process for forming integrated devices, and the cost necessary to achieve that throughput. It is always desirable to reduce the number of steps in a process for forming an integrated device or circuit as long as the integrated circuits or devices  
10 suffer no significant degradation in performance as a result thereof. Of course, a process that not only reduces the number of steps required to form an integrated device or circuit, but also increases its electrical performance, would be highly desirable.

In the annealing process it is desirable that the heating of the doped regions  
15 be uniform so that all regions are annealed similarly and so perform similarly. An annealing process should therefore have the capability to uniformly anneal doped regions of a substrate.

Another problem related to annealing is the necessity to limit the diffusion of dopant atoms within the circuit. With the ongoing reduction of device dimensions  
20 over time to achieve faster switching speeds and to incorporate more devices per unit area within the substrate, it becomes increasingly important to restrict diffusion. Otherwise, the dopant atoms can create leakage paths between source, drain and/or gate regions, which either impair or destroy electrical performance of the integrated device. It would be desirable to provide a method that reduces or eliminates diffusion  
25 of the dopant atoms in a substrate.

### **Summary of the Invention**

The invented apparatus overcomes the above-stated disadvantages. A first method of this invention includes annealing at least one region of a substrate with

charged particles. The charged particles can be electrons, protons, ions, alpha particles, or neutral particles. The substrate can be composed of a semiconductor material, for example. The particles can be p-type dopant atoms including boron (B), aluminum (Al), gallium (Ga), indium (In), and palladium (Pd), or n-type dopant atoms species including arsenic (As), phosphorus (P), antimony (Sb), titanium (Ti), platinum (Pt), gold (Au), and oxygen (O). The particles can be used to anneal dopant or other atoms previously implanted into the substrate. Alternatively, the species and energies of the particles can be selected so as to simultaneously implant and anneal the substrate. This alternative implementation of the method requires the particles be a dopant species because implantation and activation are performed in a single step. If no change in the electrical state of the substrate is required, the particles can include atoms with the same valency as the substrate. Such ions include one or more of carbon (C), silicon (Si), and germanium (Ge) or other species with the same valency as the substrate.

The particles can be applied to the substrate with a predetermined flux, energy, pulse duration, and dosage. The energy of the particles can be determined based upon one or more of the following criteria:

(1) the particles have an energy corresponding to an absorption length that is equal to or greater than the desired junction depth;

(2) the particle species is chosen to correspond to the desired dopant and the dose corresponds to the desired doping concentration; and

(3) the particles have an energy, flux, and pulse duration to raise the temperature of the irradiated region sufficiently high for annealing or activation to occur.

The energy of the charged particles can range from one-tenth (0.1) to one-hundred (100) kilo-electron-Volts, the range for electrons being from five (5) to seven (7) keV. The charged particle flux generally ranges from  $10^{12}$  to  $10^{18}$  particles per square centimeter at the surface of the substrate. The particle flux incident on the surface of the substrate can be from  $5 \times 10^{12}$  to  $5 \times 10^{13}$  particles per square

centimeter. The pulse duration of the particles can be from  $10^{-9}$  to  $10^{-4}$  seconds. However, within the limitations of presently available equipment, the pulse duration of the particles can be in a range from about  $5 \times 10^{-7}$  to  $10^{-4}$  seconds. The energy dose can be from one-tenth (0.1) to one (1.0) Joules/cm<sup>2</sup>.

- 5        The substrate can include dopants with a predetermined distribution in the substrate and the pulse duration of the particle annealing beam can be limited to prevent significant change in the dopant distribution due to diffusion. The doped region can be amorphous and the underlying substrate can be crystalline. The particle beam can produce a uniform temperature distribution in the amorphous
- 10 region sufficient to promote crystallization of the amorphous region with the crystalline portion of the substrate acting as a seed crystal. A second method of the invention includes generating particles, accelerating the particles toward a substrate, and focusing the particles to increase the flux of the particles so that they have sufficient flux and energy to anneal a region of the substrate. The particles can
- 15 include electrons, protons, ions, alpha particles, and other neutral atoms. As with the first method, the second method can be performed with a multicusp generator and an accelerator, and optional focusing lens and beam neutralizer. Most of these elements are contained in a conventional ion implanter. If the particles are dopant species, the energies, the flux and the pulse duration of the particle beam can be selected or
- 20 arranged to provide the desired dopant concentration and distribution and, possibly, to achieve activation concurrently with irradiation.

It is an object of the invention to provide an apparatus and methods that can be used to produce an even temperature distribution within a doped region undergoing treatment.

- 25        It is an object of the invention to provide an apparatus and methods that can be used to anneal a substrate without significantly changing the dopant distribution therein.

These together with other features and advantages, which will become subsequently apparent, reside in the details of the invented apparatus and method as

more fully hereinafter described and claimed, reference being made to the accompanying drawings, forming a part hereof, wherein like numerals refer to like parts throughout the several views.

## 5 **Brief Description of the Drawings**

Figs. 1A-1C are cross-sectional views of a first method of the invention to anneal a region of a substrate with charged particles;

Figs. 2A-2C are cross-sectional views of a second method of the invention to implant and anneal a region of a substrate with charged particles;

10 Fig. 3 is a cross-sectional view of an apparatus used to generate, accelerate, and focus charged particles on a substrate; and

Fig. 4 is a cross-sectional view of a magnetic filter of the apparatus of Fig. 3.

## **Description of the Preferred Embodiments**

15 As used herein, the following terms have the following meanings:

"**Absorption length**" is a well-known parameter, and is defined as the thickness of a particular material required to reduce the intensity of the energy of the charged particles propagating in such material to  $1/e$ , or 36.8 percent, of its initial intensity.

20 "**Anneal**" includes "activation", "crystallization" or "recrystallization" within its scope, and refers to raising and subsequently lowering the temperature of a relatively disordered semiconductor region either to crystallize such region to be integral with a crystalline semiconductor substrate, to activate dopant atoms by incorporating them into the crystalline lattice of the semiconductor region, or to incorporate metal atoms  
25 within a semiconductor region to form relatively low-resistance silicide contacts to coupled conductive lines to a terminal of an integrated device formed on a substrate.

"**Depth**" refers to the distance to which a region extends into the substrate in a direction perpendicular to the substrate's upper major surface as viewed in the perspective of the Figures.

**"Disordered region"** refers to a semiconductor region in which the atoms contained therein are not relatively ordered either because they are in a non-crystalline state due to damage or amorphization caused by implantation of dopant or non-dopant atoms, and/or because they include dopant atoms that have not been  
5 incorporated into the crystalline lattice of such region by activation, for example.

**"Energy"** refers to the kinetic energy of the charged particles gained by an acceleration applied to an implantation machine.

**"Flux"** refers to the number of charged particles per unit area (usually expressed in particles per square centimeter units) incident to the top surface of the  
10 region of the substrate to be annealed.

**"Integrated device"** can refer to an active device such as a transistor or diode (including bipolar and complementary metal-insulative semiconductor field-effect transistor (MISFET)), or can refer to a passive device such as a resistor or capacitor. A MISFET includes a complementary metal-oxide field-effect transistor  
15 (MOSFET) within its definition.

**"Power"** as applied to charged particles, is defined as the flux of the charged particles multiplied by the energy of the charged particles, further multiplied by the pulse duration over which the charged particles are applied to the substrate.

**"Process"** as used herein is broadly defined and generally refers to a method  
20 for forming integrated devices or circuits on a substrate.

**"Thermal diffusion coefficient"** is defined as  $k / \rho \cdot C$  where  $k$  is the thermal conductivity (J/sec $\cdot$ cm $\cdot$ °C),  $\rho$  is the density (gm/cm<sup>3</sup>), and  $C$  is the heat capacity (J/gm $\cdot$ °C).

**"Thickness"** refers to the distance between the upper and lower surfaces of a  
25 region in a direction perpendicular to such surfaces as viewed in the perspective of the Figures.

## 1. First Generalized Embodiment of the Invented Method

Figs. 1A - 1C are views of a method for annealing a region of a semiconductor

substrate using charged particles. The semiconductor substrate 1 can include silicon, gallium arsenide, or other semiconductor material. The substrate 1 can be in the form of a wafer, for example. The wafer can be six, eight, twelve, or sixteen inches in diameter and on the order of seven-hundred-fifty microns in thickness. Alternatively, the substrate 1 can be a layer of semiconductor material formed on an insulative layer or material. For example, the substrate 1 could be a layer of silicon in a silicon-on-insulator (SOI) or a silicon-on-sapphire (SOS) structure. The substrate 1 could also be a relatively thin-film of semiconductor material such as poly-silicon or crystalline silicon formed on an insulative substrate as is used in the manufacture of thin-film transistors for flat panel displays and the like. The major surface 3 of the substrate 1 extends in a direction perpendicular to the plane of Figs. 1A - 1C. Such major surface can be oriented in  $\langle 100 \rangle$  or  $\langle 111 \rangle$  directions defined by Miller indices, as is well-known to persons of ordinary skill in this technology.

In Fig. 1A, the substrate 1 is implanted with atoms 4. The atoms 4 are distributed in region 2 of the substrate to a predetermined depth corresponding to the thickness  $t$  of the region 2. The major surface 3 of the substrate 1 is the entry point of the particle beam. In general, the greater the atomic weight of such atoms and the less the implantation energy of such atoms, the shallower the depth to which the implanted atoms travel into the substrate 1 relative to its major surface 3. Conversely, the less the atomic weight of the implanted atoms and the greater their implantation energy, the greater the thickness  $t$  to which the region 2 extends into the substrate 1.

The method step of Fig. 1A can be used to implant dopant atoms to form a source or drain region of an integrated transistor in the substrate 1, or to form the cathode or anode of a diode, for example. Such step can also be used to implant dopant atoms into the substrate 1 to form an integrated resistor, or conductive portions of an integrated capacitor, or a conductive line extending between terminals of integrated devices that are coupled together, for example. If the implanted atoms are used to dope the region 2 such atoms are either p-type or n-type. For a silicon

substrate 1, p-type dopant atoms include boron (B), aluminum (Al), gallium (Ga), indium (In), and palladium (Pd), and n-type dopant atoms species include arsenic (As), phosphorus (P), antimony (Sb), titanium (Ti), platinum (Pt), gold (Au), and oxygen (O). The composition of silicides and the species of refractory metal atoms that can be used to form such silicides are described in, for example, U.S. Patent No. 5,888,888 issued March 30, 1999 to Somit Talwar et al., which is incorporated herein by reference. In addition the atoms 4 implanted into the region 2 can be electrically neutral species such as silicon (Si) or germanium (Ge) in silicon substrate 1. For example, the atoms can be implanted into the region 2 of the substrate 1 using a device such as the 9500 XR Ion Implanter machine commercially-available from Applied Materials, Inc. of San Jose, California.

The implantation of the atoms of Fig. 1A into the substrate 1 damages the crystallinity of the substrate in the region 2 affected by the implanted atoms. More specifically, the implanted atoms collide with atoms of the substrate 1 and break chemical bonds between some of the substrate atoms, rendering the substrate atoms relatively disordered in the region 2. The atoms in the region 2 can be so disordered as to be considered 'amorphous' and thus lacking any significant crystallinity. In addition, the implanted atoms are generally not positioned within the crystalline lattice of the substrate 1 within the region 2 immediately after they are implanted. If the implanted atoms are dopant species it is especially important to anneal the region 2 to restore the crystallinity of the substrate 1 and to position at least some of the implanted atoms at sites within the lattice of the semiconductor material. Otherwise, the region 2 may not exhibit the desired electrical performance upon application of electric current/voltage thereto. Accordingly, the region 2 must be annealed to obtain proper electrical performance. The manner of accomplishing this in the subject method is now described with reference to Fig. 1B.

Importantly, in Fig. 1B, the method includes a step of annealing the region 2 with particles. The particles can be electrons, protons, ions, alpha particles, or neutral atoms. The different types of atoms that can be used in this step can include virtually



any ion that does not adversely affect electrical performance. Such atoms can include dopant species such as boron (B), aluminum (Al), gallium (Ga), indium (In), and palladium (Pd), arsenic (As), phosphorus (P), antimony (Sb), titanium (Ti), platinum (Pt), gold (Au), and oxygen (O). The atom species can also be one that is electrically neutral such as silicon (Si), or germanium (Ge). These atomic species have the same valence as common substrate materials such as silicon and thus have no significant impact upon the electrical characteristics of the region 2. Other atomic species include the noble gases such as helium (He), neon (N), argon (Ar), xenon (Xe), or krypton (Kr), for example. Hydrogen (H) and nitrogen (N) are also candidates. These gaseous species provide the advantage of being relatively easy to ionize. Fluorine (F) and chlorine (Cl) could also be used but are generally less preferred because of their toxic and highly reactive natures. Sodium (Na) and potassium (K) are generally shunned for use in semiconductor materials because they adversely affect electrical performance of integrated devices. Because the flux of the charged particles necessary to anneal the region 2 is relatively small (approximately  $10^{13}$  to  $10^{18}$  particles per square centimeter) almost any atomic species can be used to anneal the region 2 of the substrate 1.

The energy, flux, and pulse duration of the particle beam 6 that is required to anneal the region 2 generally depends upon the thickness  $t$  of the region 2 and the type of material composing such region 2. In general the greater the energy of the particles, the greater the thickness  $t$  to which the particles can anneal the region 2. Conversely, the less the energy of the particles the less the thickness  $t$  to which the region 2 extends.

Exemplary ranges for the energy, flux, and pulse duration of the particles 6 applied to anneal the region 2 are as follows. The energy of the charged particles 4 can range from one-tenth (0.1) to one-hundred (100) kilo-electronVolts, the most preferred range being from five (5) to seven (7) keV for electrons. The charged particle dose generally ranges from  $10^{12}$  to  $10^{18}$  particles per square centimeter at the surface 3 of the substrate 1. Determination of the pulse duration of the particle

irradiation 4 to be applied to the region 2 should utilize the following considerations.

In general, it is desirable to heat the region 2 uniformly from surface 3 to lower surface 5 of the dopant profile. The pulse duration should also be sufficiently limited so that minimal dopant diffusion occurs and so that the thermal diffusion length is

5 limited to the desired annealing depth or by practical limitations of the equipment.

This minimizes the power required to anneal the region. In general, the pulse duration can be from  $10^{-9}$  to  $10^{-4}$  seconds. However, commercially-available implantation equipment limits the pulse duration to a range from about  $5 \times 10^{-7}$  to  $10^{-4}$  seconds. With implantation equipment currently available greater implant voltages

10 and longer pulse times than the most preferred pulse duration are required.

Nonetheless, effective annealing can be performed with the limitations of commercially-available equipment.

For example, if the thickness  $t$  is one-hundred (100) nanometers and electrons are used for the particles 6, a six (6) kiloelectronVolt (keV) electron beam that is

15 pulsed for one (1) microsecond can be used which penetrates four-tenths (0.4) microns into the substrate 1. Such penetration depth is four times the thickness  $t$  of the region 2. In this case thermal diffusion resulting from the electrons heats the substrate to nine-and-one-half (9.5) microns, which is much thicker than the thickness  $t$ . Nonetheless, the pulse duration is sufficiently short to anneal the region 2 without  
20 degradation in the dopant profile.

As another example protons can be used as the particles 6 with an energy of 6 keV. If the thickness  $t$  of the region 2 is one-hundred (100) nanometers the proton range into the substrate 1 is about the same as the thickness  $t$  of the region 2. The thermal diffusion length for the protons in this example is 9.5 microns for a pulse  
25 duration of one (1) microsecond, almost one hundred times as great as the thickness of the region 2 in this example. However the pulse duration is sufficiently short to prevent dopant diffusion beyond the region 2.

Particles larger than protons would be even less penetrating into the substrate but limitations of commercially-available equipment would limit the pulse duration to

the microsecond region.

The following tables illustrate range versus kinetic energy for various charged particles in a silicon substrate:

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Table 1. Range of Protons in Silicon

Kinetic Energy (keV)	Range (microns)
6	0.08
12	0.15
18	0.20

10

Table 2. Range of Electrons in Silicon

Kinetic Energy (keV)	Range (microns)
6	0.4
12	1.1
18	2.2

15

Table 3. Range of Alpha Particles in Silicon

Kinetic Energy (keV)	Range (microns)
6	0.063
12	0.12
18	0.18

20

25

Determining kinetic energies corresponding to other ranges than those stated above can be performed by interpolation or extrapolation of the kinetic energies and ranges. In addition such information is available from numerous sources including National

Institutes of Standards (NIST) and others.

The exposure of the region 2 to the particle beam 6 heats the region to near its melting point so that the atoms therein can rearrange themselves into a relatively ordered state as compared to that resulting from performance of the implanting step in Fig. 1A. Therefore if the region 2 contains dopant atoms such atoms rearrange themselves with the substrate atoms so that the dopant atoms are electrically active.

If the region 2 is composed of amorphous atoms of the substrate material and the substrate 1 is crystalline, the power imparted by the particle beam 6 can be sufficient to trigger the recrystallization of the region 2 to the surface 5. Depending on the temperature distribution in the amorphous region the substrate 1 can act as a seed upon which the region 2 crystallizes so that is crystallographically integral with the substrate 1. In Fig. 1C, the region 2 is illustrated with 'x' hatching to indicate that such region is relatively ordered after annealing with the charged particles 6.

Figs. 2A - 2C are a second embodiment of the invented method. In Fig. 2A, the substrate 1 is shown in its initial state before performance of the second embodiment of the method. In Fig. 2B, particles 6 are implanted into the substrate 1 to form the region 2. The particles 6 can be a dopant. The energy, flux, and pulse duration of the particle beam 6 is sufficient to implant these particles into the region 2, and also to anneal the region. Accordingly the region 2 can be doped and activated in one step, a significant advantage over previous methods. In Fig. 2C, the region 2 crystallizes after being triggered by the charged particle beam 6 so that the region 2 crystallizes with in a relatively ordered form represented by the 'x' hatching in Fig. 2C.

An option for the generation of electrons, protons, or other ions is a multicusp plasma generator 106 shown in Fig. 3. The multicusp generator 106 includes an element 108 that can be a direct current (DC) or radio-frequency (RF) element. The element 108 is coupled to a DC or RF source via conductors 110. The water-cooled jacket 112 surrounds the element 108 and circulates a flow of coolant such as water to cool portions of the generator 106 relatively near the element 108. The conduit



charged particles desired to be used have positive or negative charges. Voltage is also applied to the elements of the accelerator unit 124 to accelerate and focus the charged particles at the substrate 10. The charged particles collide with and anneal one or more regions of the substrate 10.

- 5           The many features and advantages of the present invention are apparent from the detailed specification and thus, it is intended by the appended claims to cover all such features and advantages of the described methods and article produced thereby which follow in the true spirit and scope of the invention. Further, since numerous modifications and changes will readily occur to those of ordinary skill in the art, it is
- 10 not desired to limit the invention to the exact construction and operation illustrated and described. Accordingly, all suitable modifications and equivalents may be resorted to as falling within the spirit and scope of the claimed invention.